

**Amendments to the Claims:**

Please amend the claims as follows:

Claims 1-26 (Canceled).

Claim 27 (Currently Amended): A method of adjusting a frequency of a dot clock signal for a video signal, said method comprising ~~the steps of:~~

- (a) ~~multiplying~~ generating a frequency of first dot clock signal based on a horizontal synchronizing signal of said video signal ~~[[by]]~~ and a first factor ~~to generate, the first factor~~ representing a ratio of a frequency of the ~~[[a]]~~ first dot clock signal to a frequency of the horizontal synchronizing signal;
- (b) sampling said video signal by said first dot clock signal to obtain image data;
- (c) obtaining a number of beats caused by a difference between a desirable frequency and the actual frequency of the first dot clock signal over one line of said image data;
- (d) correcting said first factor with said number of beats, thereby obtaining a ~~desirable~~ second factor; and
- (e) ~~multiplying the frequency of~~ generating a second dot clock signal based on said horizontal synchronizing signal ~~[[by]]~~ and said second factor ~~to generate a second dot clock signal that can be used to sample image data without beats.~~

Claims 28-45 (Canceled).

Claim 46 (Currently Amended): An apparatus for adjusting a frequency of a dot clock signal for a video signal, comprising:

~~dot clock generation~~ means for ~~multiplying~~ generating a first dot clock signal based on a frequency of a horizontal synchronizing signal of said video signal [[by]] and a first factor to generate a first dot clock signal, the first factor representing a ratio of a frequency of the first dot clock signal to a frequency of the horizontal synchronizing signal;

~~sampling~~ means for sampling said video signal by said first dot clock signal to obtain image data;

~~first operation~~ means for obtaining a number of beats caused by a difference between a desirable frequency and the actual frequency of the first dot clock signal over one line of said image data;

~~second operation~~ means for correcting said first factor with said number of beats, thereby obtaining a ~~desirable~~ second factor; and

~~factor setting~~ means for setting said second factor in said ~~dot clock generation~~ means for generating and thereby enabling said means for generating to multiply the frequency of to generate a second dot clock signal based on said horizontal synchronizing signal [[by]] and said second factor ~~to generate a second dot clock signal that can be used to sample image data without beats.~~

Claims 47-68 (Canceled).